

# UTMC ERRATA

## UT63M14X MIL-STD-1553A/B 5-VOLT TRANSCEIVER

The following table has been amended to show updated  $I_{CC}$  maximum numbers. Number in brackets indicates previous values.

### DC ELECTRICAL CHARACTERISTICS <sup>1</sup>

$V_{CC} = 5.0V \pm 10\%$

$-55^{\circ}C < T_C < +125^{\circ}C$

SYMBOL	PARAMETER	MINIMUM	MAXIMUM	UNIT	CONDITION
$V_{IL}$	Input low voltage		0.8	V	RXEN, TXIHB, TXIN, $\overline{TXIN}$
$V_{IH}$	Input high voltage	2.0		V	RXEN, TXIHB, TXIN, $\overline{TXIN}$
$I_{IL}$	Input low current	-0.1		mA	$\frac{V_{IL}}{TXIN} = 0.4V$ ; RXEN, TXIHB, TXIN, $\overline{TXIN}$
$I_{IH}$	Input high current	-40	40	$\mu A$	$\frac{V_{IH}}{TXIN} = 2.7V$ ; RXEN, TXIHB, TXIN, $\overline{TXIN}$
$V_{OL}$	Output low voltage		.55	V	$I_{OL} = 4mA$ ; RXOUT, $\overline{RXOUT}$
$V_{OH}$	Output high voltage	2.4		V	$I_{OH} = 0.4mA$ ; RXOUT, $\overline{RXOUT}$
$I_{CC}$	$V_{CC}$ supply current		22 190 [185] 360 [345] 590 [570] 680 [650]	mA mA mA mA mA	0% duty cycle (non-transmitting) 25% duty cycle ( $f = 1MHz$ ) 50% duty cycle ( $f = 1MHz$ ) 87.5% duty cycle ( $f = 1MHz$ ) 100% duty cycle ( $f = 1MHz$ ) <sup>2</sup>

**Note:**

1. All tests guaranteed per test figure 6.
2. Guaranteed but not tested.