

# Aeroflex Colorado Springs Application Note

## Bus Timing Requirements for JTAG Interface to UT69151 S $\mu$ MMIT<sup>TM</sup>

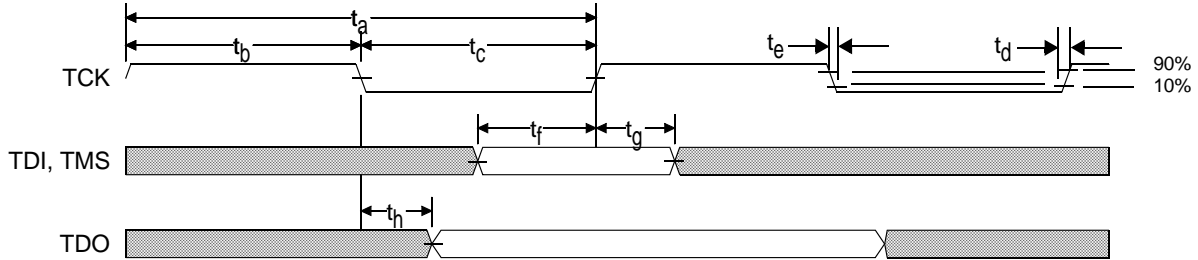


Figure 1. AC Timing Diagram

Table 1: AC Electrical Characteristics

Test	Symbol	Limits		Unit
		Min	Max	
TCK frequency			1	MHz
TCK period	$t_a$	1000		ns
TCK high time	$t_b$	$0.5*t_a$		
TCK low time	$t_c$	$0.5*t_a$		
TCK rise time	$t_d$		5	
TCK fall time	$t_e$		5	
TDI, TMS setup time	$t_f$	250		
TDI, TMS hold time	$t_g$	250		
TDO valid delay	$t_h$	250		